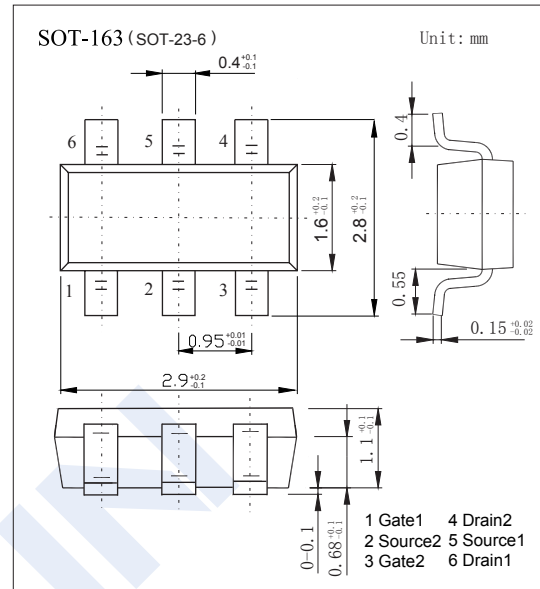
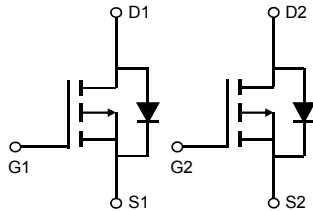


## Dual P-Channel MOSFET

## AO6801A (KO6801A)

## ■ Features

- $V_{DS} (V) = -30V$
- $I_D = -2.3A (V_{GS} = -10V)$
- $R_{DS(ON)} < 115m\Omega (V_{GS} = -10V)$
- $R_{DS(ON)} < 150m\Omega (V_{GS} = -4.5V)$
- $R_{DS(ON)} < 200m\Omega (V_{GS} = -2.5V)$

■ Absolute Maximum Ratings  $T_A = 25^\circ C$ 

Parameter	Symbol	Rating	Unit
Drain-Source Voltage	$V_{DS}$	-30	V
Gate-Source Voltage	$V_{GS}$	$\pm 12$	
Continuous Drain Current	$I_D$	$T_A = 25^\circ C$	-2.3
		$T_A = 70^\circ C$	-2
Pulsed Drain Current	$I_{DM}$	-11	A
Power Dissipation	$P_D$	$T_A = 25^\circ C$	1.15
		$T_A = 70^\circ C$	0.73
Thermal Resistance.Junction- to-Ambient	$R_{thJA}$	$t \leq 10s$	110
		Steady-State	150
Thermal Resistance.Junction- to-Lead	$R_{thJL}$	80	$^\circ C/W$
Junction Temperature	$T_J$	150	$^\circ C$
Junction Storage Temperature Range	$T_{stg}$	-55 to 150	

## Dual P-Channel MOSFET

### AO6801A (KO6801A)

#### ■ Electrical Characteristics Ta = 25°C

Parameter	Symbol	Test Conditions	Min	Typ	Max	Unit	
Drain-Source Breakdown Voltage	V <sub>DSS</sub>	I <sub>D</sub> =-250 μA, V <sub>GS</sub> =0V	-30			V	
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> =-30V, V <sub>GS</sub> =0V			-1	μA	
		V <sub>DS</sub> =-30V, V <sub>GS</sub> =0V, T <sub>J</sub> =55°C			-5		
Gate-Body leakage current	I <sub>GSS</sub>	V <sub>DS</sub> =0V, V <sub>GS</sub> =±12V			±100	nA	
Gate Threshold Voltage	V <sub>GS(th)</sub>	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =-250 μA	-0.6		-1.4	V	
Static Drain-Source On-Resistance	R <sub>DS(on)</sub>	V <sub>GS</sub> =-10V, I <sub>D</sub> =-2.3A			115	mΩ	
		V <sub>GS</sub> =-10V, I <sub>D</sub> =-2.3A, T <sub>J</sub> =125°C			190		
		V <sub>GS</sub> =-4.5V, I <sub>D</sub> =-2A			150		
		V <sub>GS</sub> =-2.5V, I <sub>D</sub> =-1A			200		
On state drain current	I <sub>D(on)</sub>	V <sub>GS</sub> =-10V, V <sub>DS</sub> =-5V	-11			A	
Forward Transconductance	g <sub>FS</sub>	V <sub>DS</sub> =-5V, I <sub>D</sub> =-2.3A		8		S	
Input Capacitance	C <sub>iss</sub>	V <sub>GS</sub> =0V, V <sub>DS</sub> =-15V, f=1MHz		260	315	pF	
Output Capacitance	C <sub>oss</sub>			37			
Reverse Transfer Capacitance	C <sub>rss</sub>			20			
Gate resistance	R <sub>g</sub>		V <sub>GS</sub> =0V, V <sub>DS</sub> =0V, f=1MHz	4			12
Total Gate Charge (10V)	Q <sub>g</sub>	V <sub>GS</sub> =-10V, V <sub>DS</sub> =-15V, I <sub>D</sub> =-2.3A		5.9	7	nC	
Total Gate Charge (4.5V)				2.8	4		
Gate Source Charge			Q <sub>gs</sub>		0.7		
Gate Drain Charge			Q <sub>gd</sub>		1		
Turn-On DelayTime	t <sub>d(on)</sub>	V <sub>GS</sub> =-10V, V <sub>DS</sub> =-15V, R <sub>L</sub> =6.5Ω, R <sub>GEN</sub> =3Ω		6		ns	
Turn-On Rise Time	t <sub>r</sub>			3.5			
Turn-Off DelayTime	t <sub>d(off)</sub>			20			
Turn-Off Fall Time	t <sub>f</sub>			5			
Body Diode Reverse Recovery Time	t <sub>rr</sub>	I <sub>F</sub> =-2.3A, dI/dt=100A/μs		11.5	15	ns	
Body Diode Reverse Recovery Charge	Q <sub>rr</sub>			4.5			
Maximum Body-Diode Continuous Current	I <sub>S</sub>				-1.5	A	
Diode Forward Voltage	V <sub>SD</sub>	I <sub>S</sub> =-1A, V <sub>GS</sub> =0V			-1	V	

\* The static characteristics in Figures 1 to 6 are obtained using <300us pulses, duty cycle 0.5% max.

#### ■ Marking

Marking	81**
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## Dual P-Channel MOSFET AO6801A (KO6801A)

■ Typical Characteristics

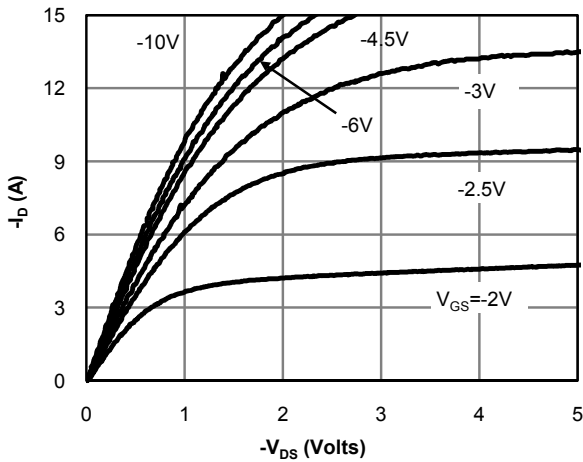


Fig 1: On-Region Characteristics (Note E)

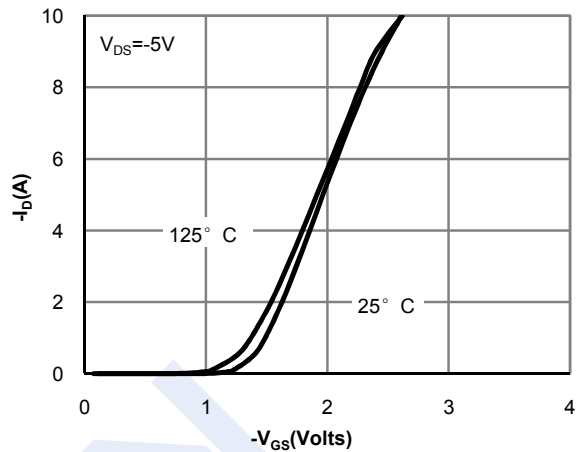


Figure 2: Transfer Characteristics (Note E)

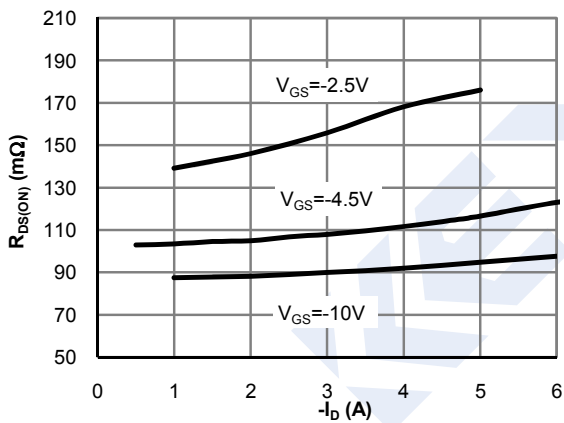


Figure 3: On-Resistance vs. Drain Current and Gate Voltage (Note E)

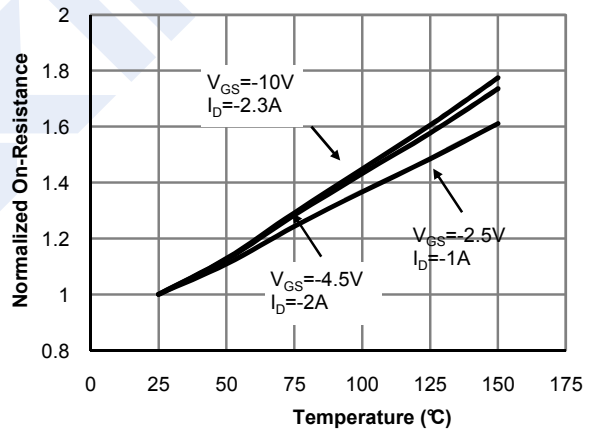


Figure 4: On-Resistance vs. Junction Temperature (Note E)

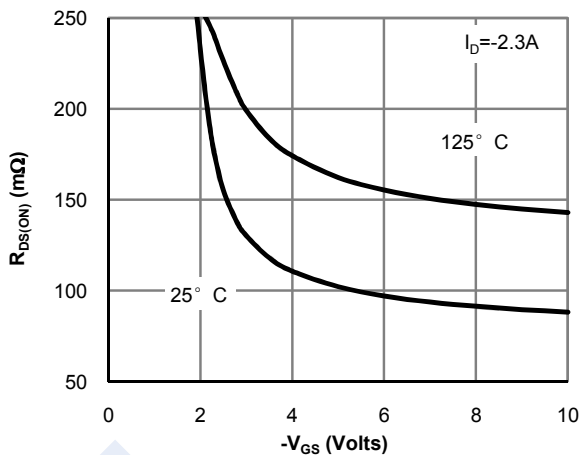


Figure 5: On-Resistance vs. Gate-Source Voltage (Note E)

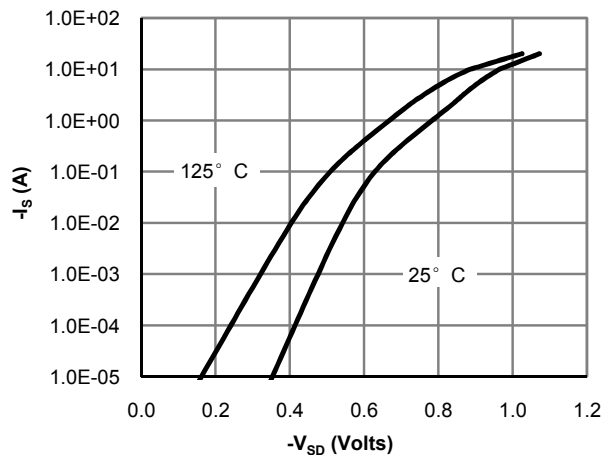


Figure 6: Body-Diode Characteristics (Note E)

## Dual P-Channel MOSFET AO6801A (KO6801A)

■ Typical Characteristics

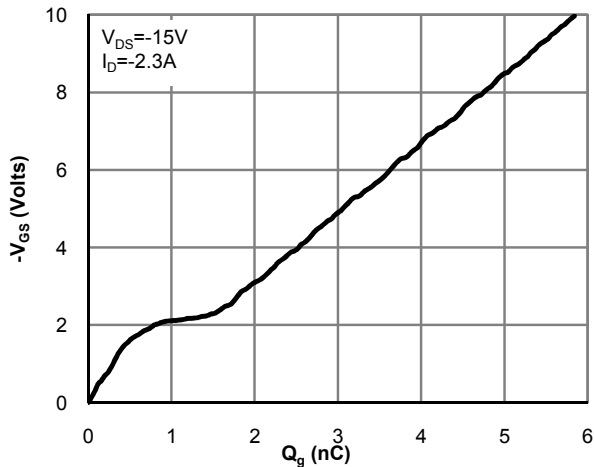


Figure 7: Gate-Charge Characteristics

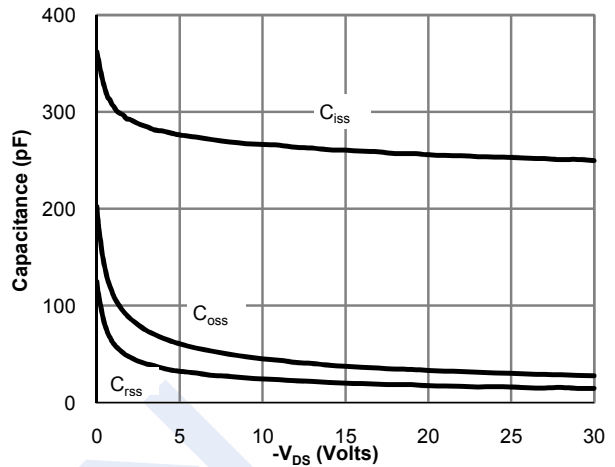


Figure 8: Capacitance Characteristics

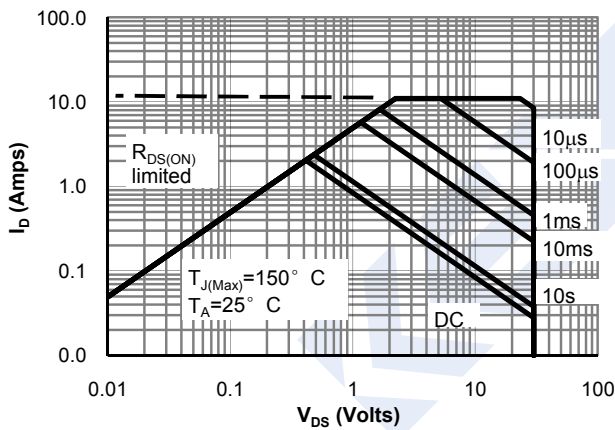


Figure 9: Maximum Forward Biased Safe Operating Area (Note F)

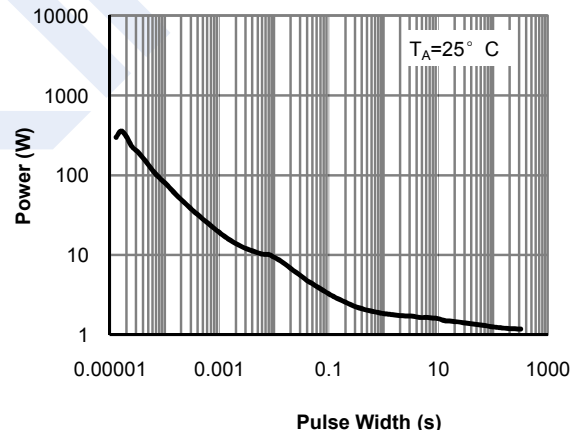


Figure 10: Single Pulse Power Rating Junction-to-Ambient (Note F)

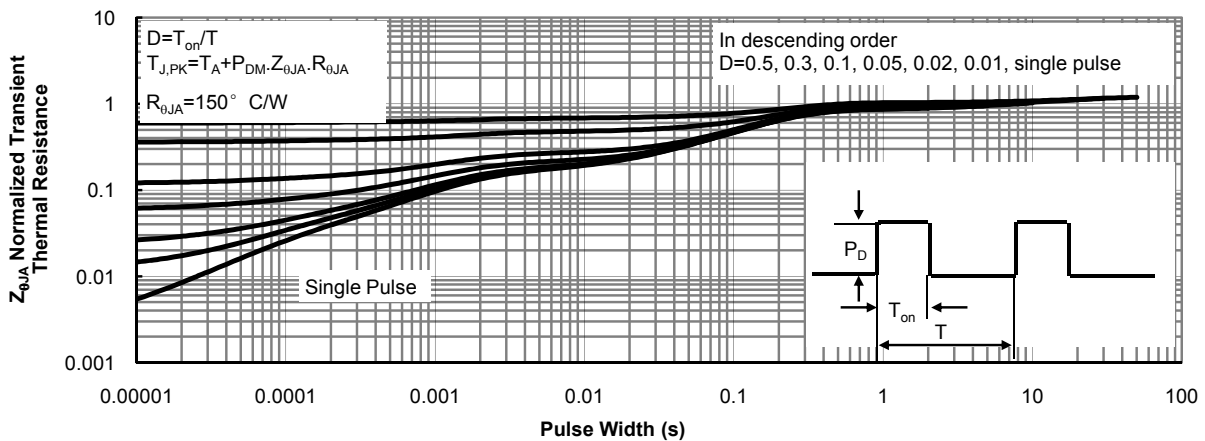


Figure 11: Normalized Maximum Transient Thermal Impedance (Note F)